

Microcontroller Design Manual

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A key design goal was achieving low-latency input/output (interrupt) M-profile, the "Microcontroller" profile, implemented by most cores in the Cortex-M series.
http://en.wikipedia.org/wiki/ARM_architecture

Intel MCS-51 - Wikipedia, the free encyclopedia -

The Intel MCS-51 (commonly referred to as 8051) is a Harvard architecture, CISC instruction set, single chip microcontroller (C) series which was developed by Intel
http://en.wikipedia.org/wiki/Intel_MCS-51

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